

FIG. 1

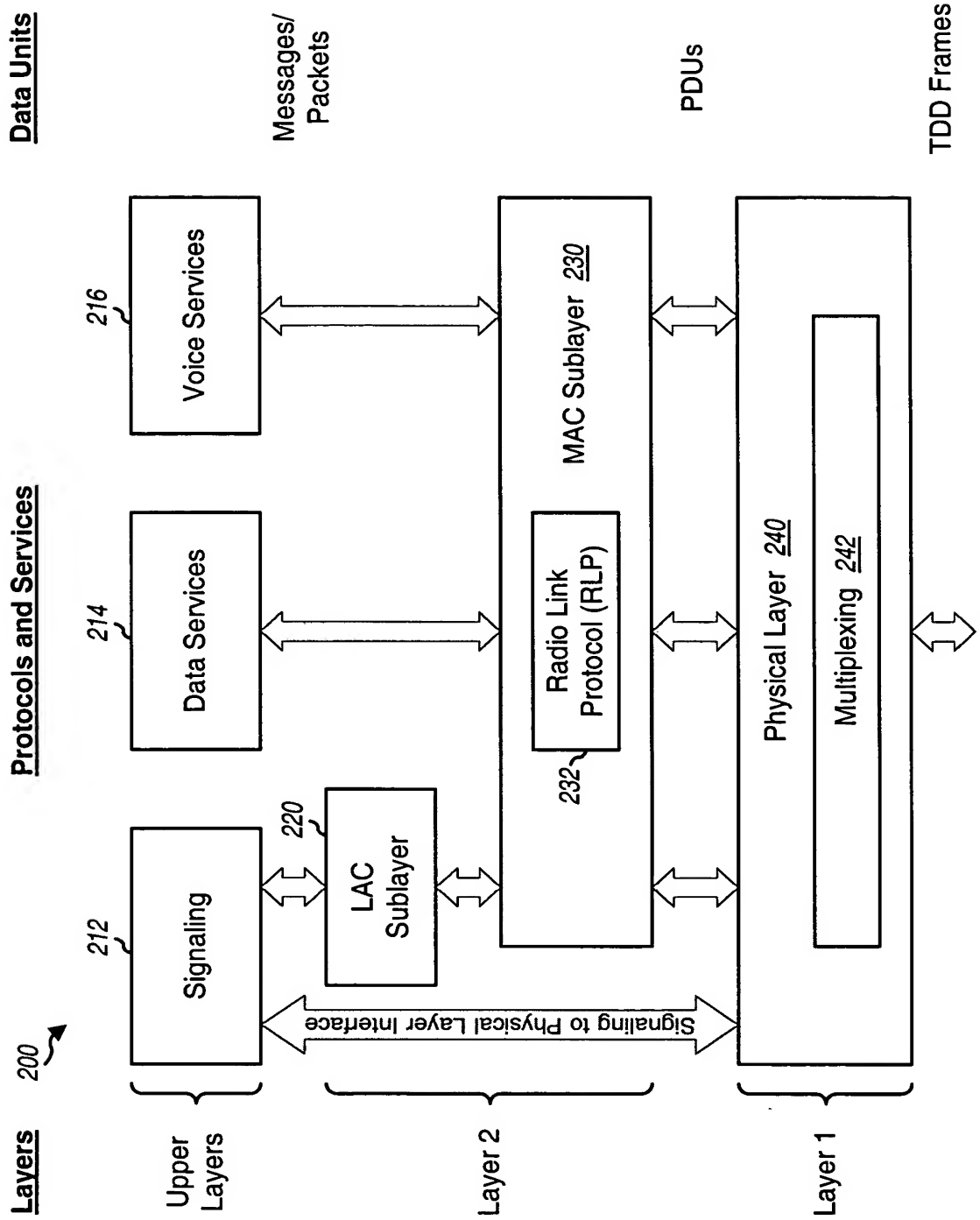


FIG. 2

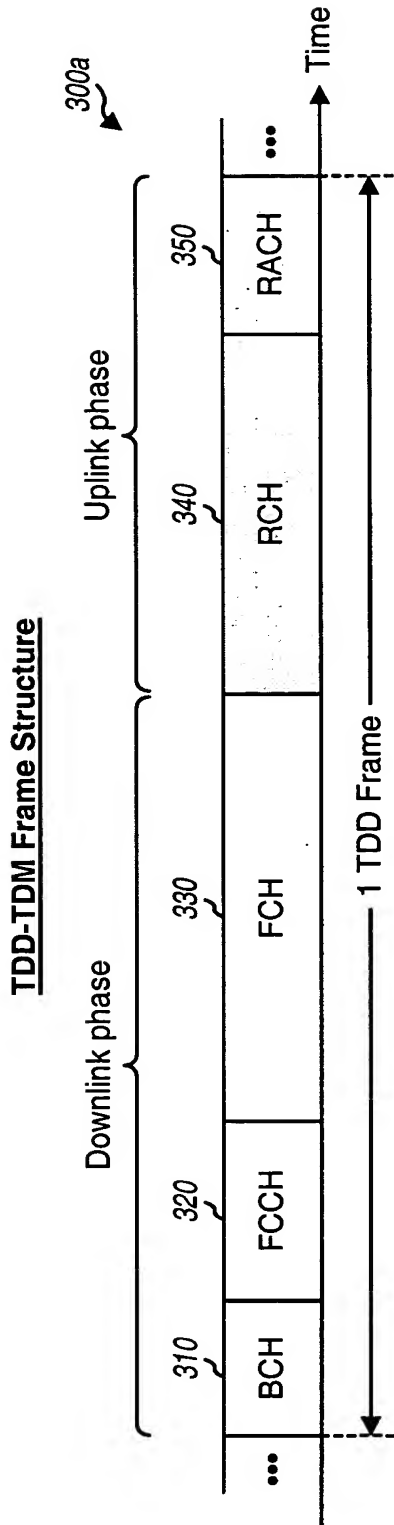


FIG. 3A

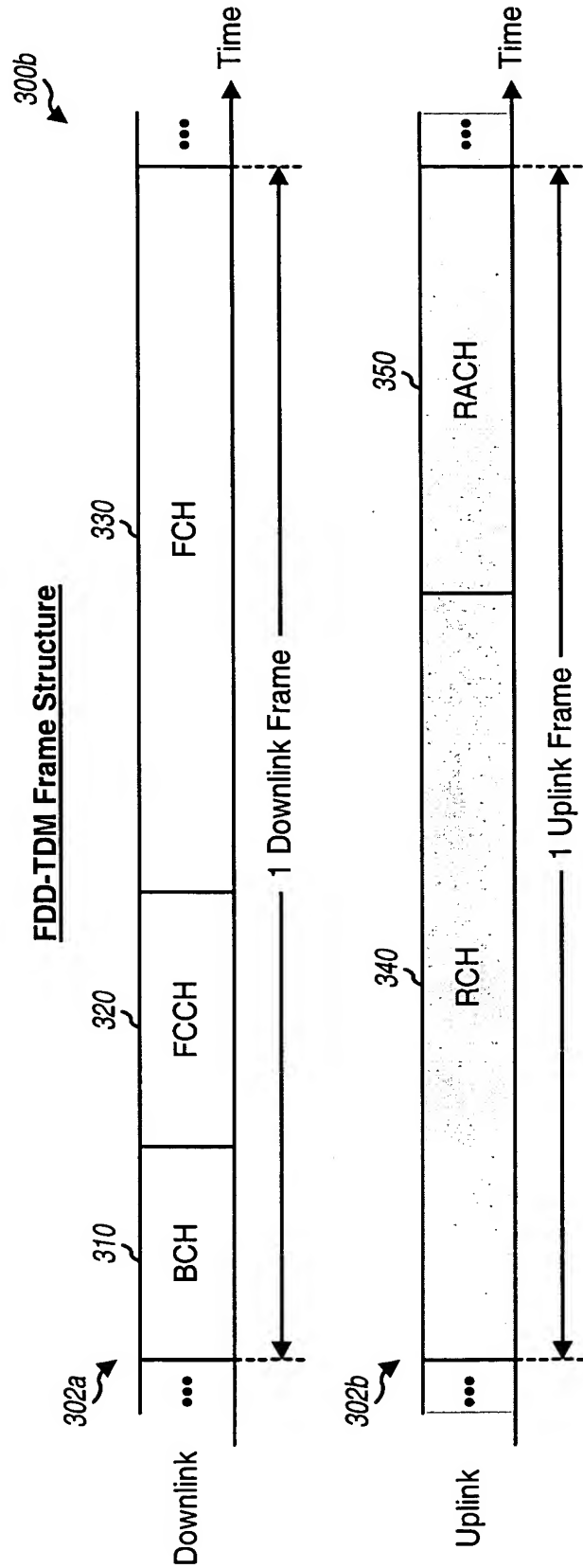


FIG. 3B

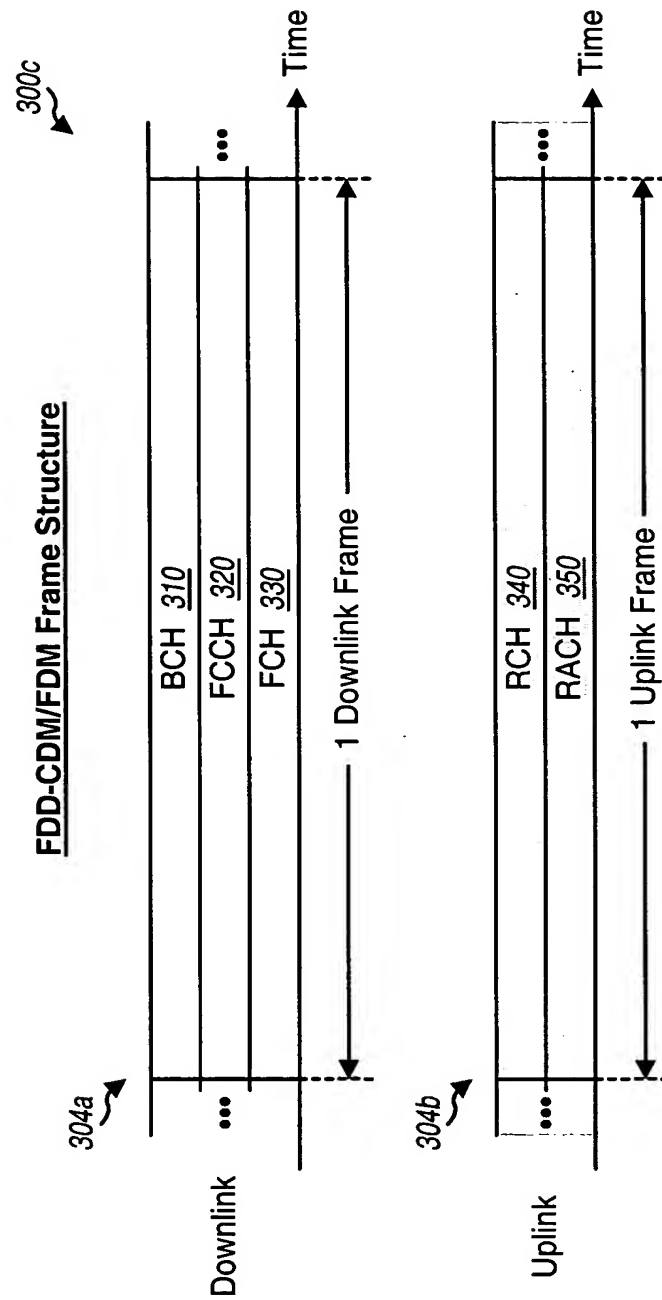


FIG. 3C

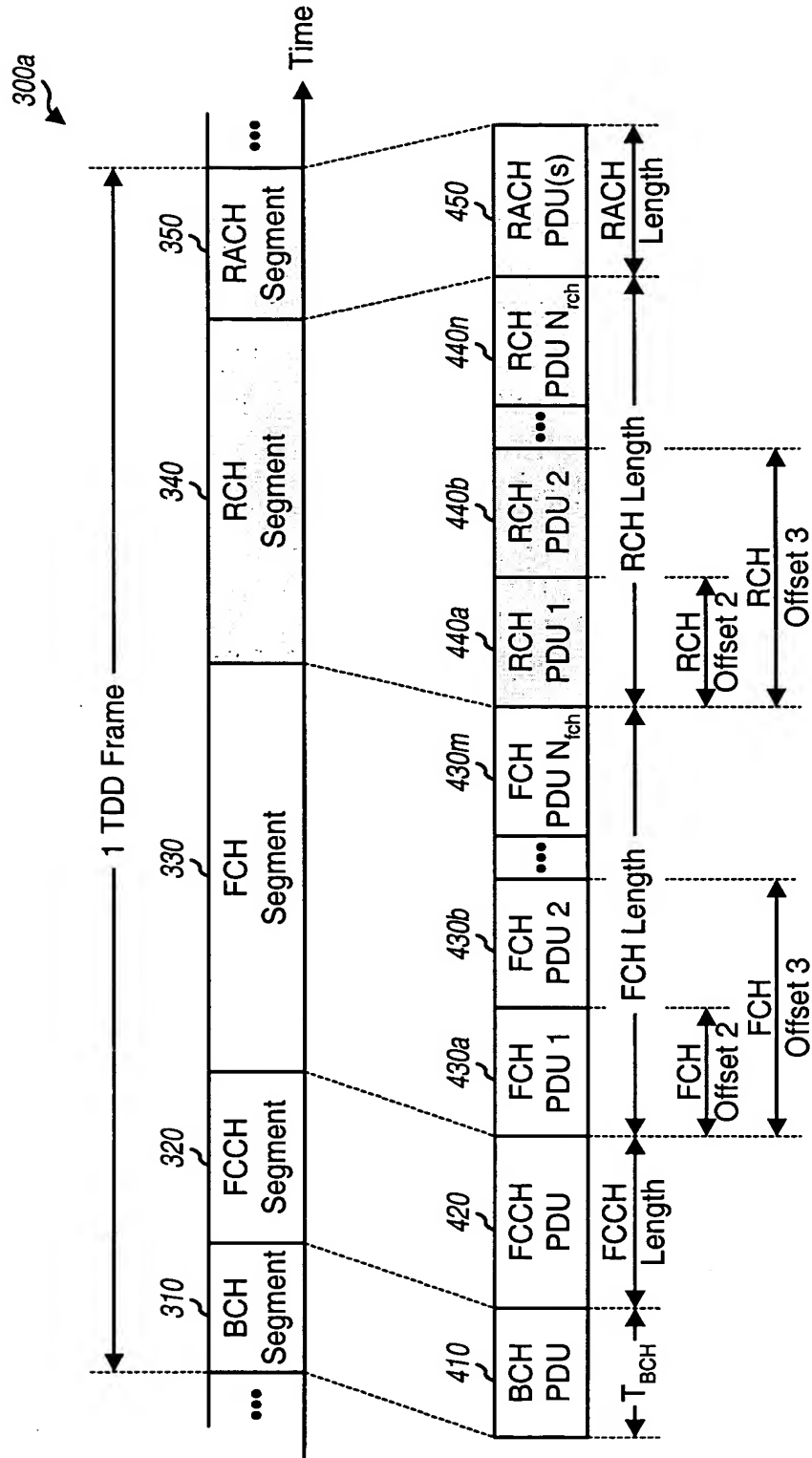


FIG. 4

Sheet 6/ 23

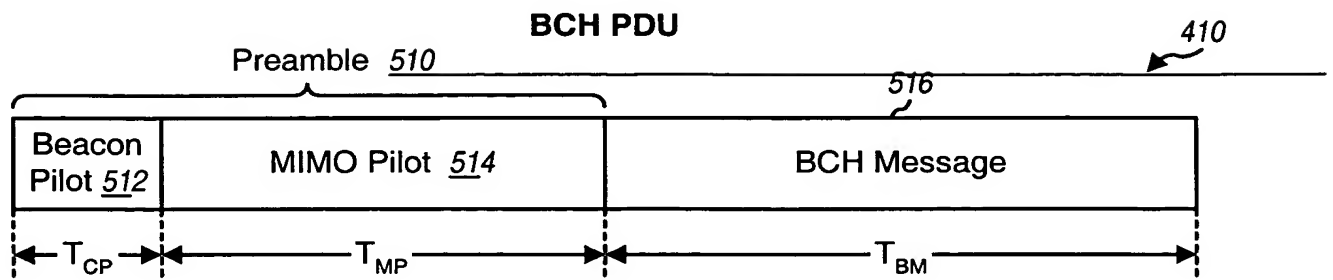


FIG. 5A

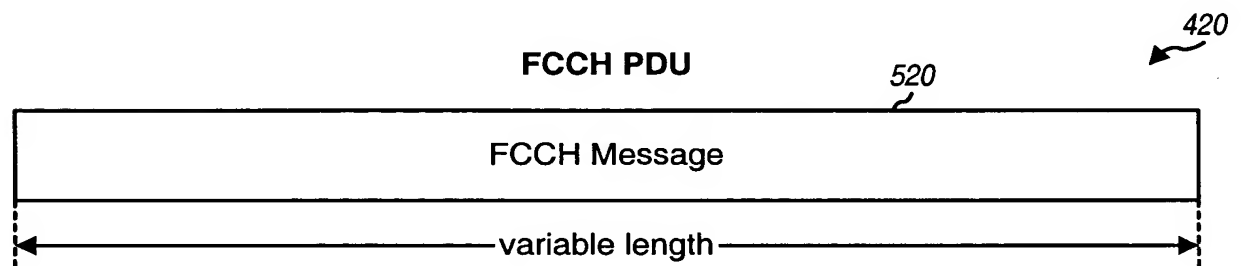


FIG. 5B

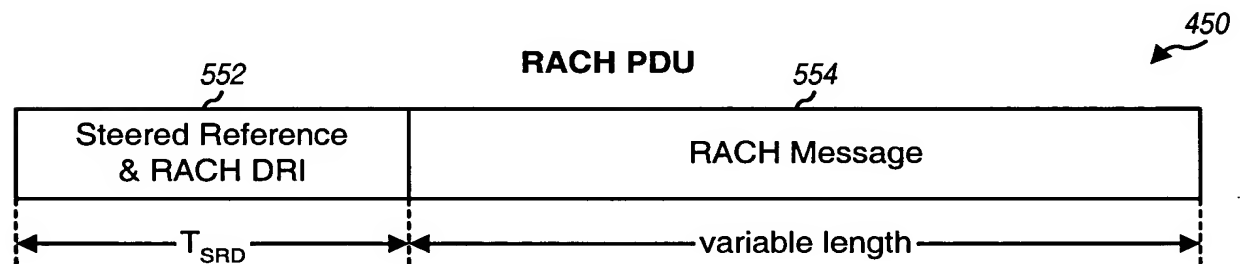


FIG. 5C

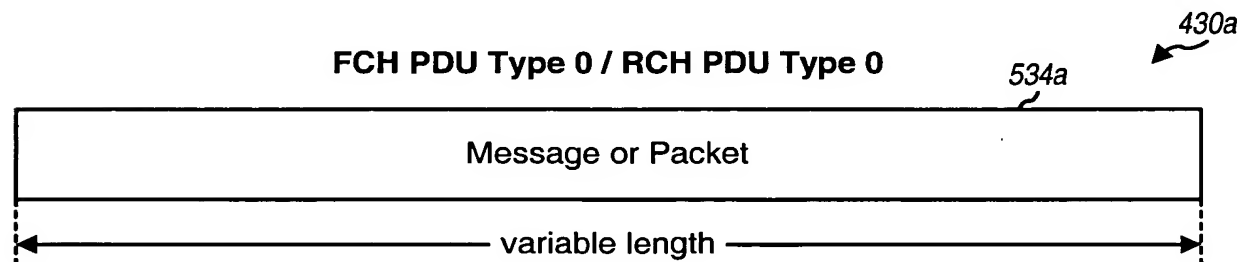
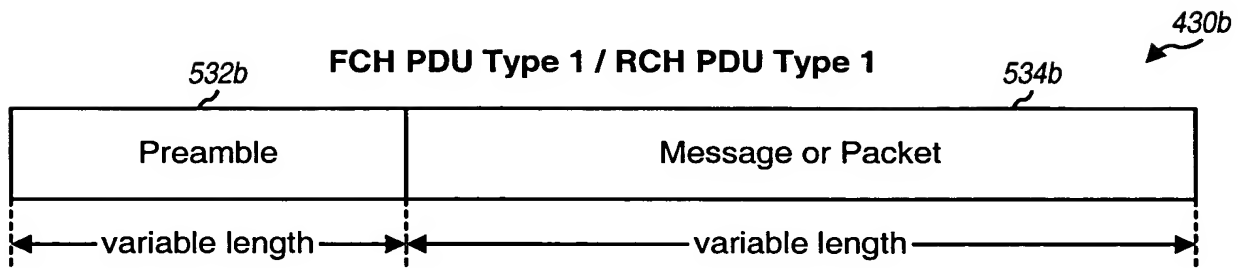
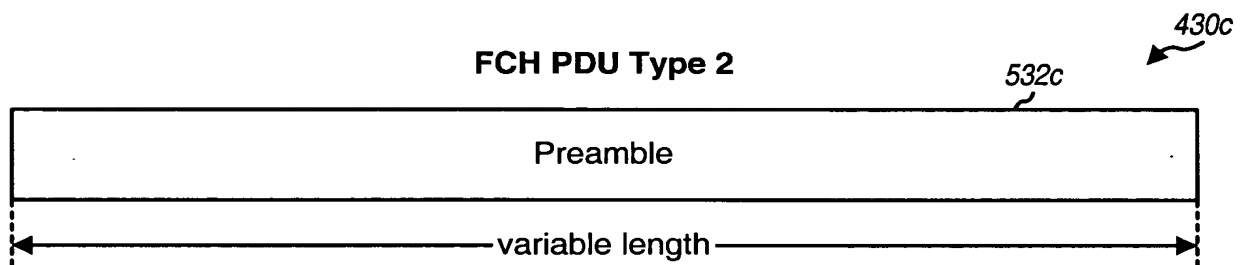
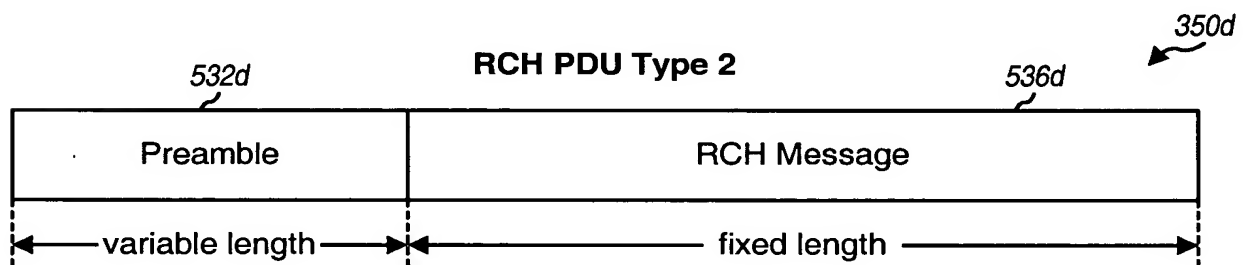


FIG. 5D

**FIG. 5E****FIG. 5F****FIG. 5G**

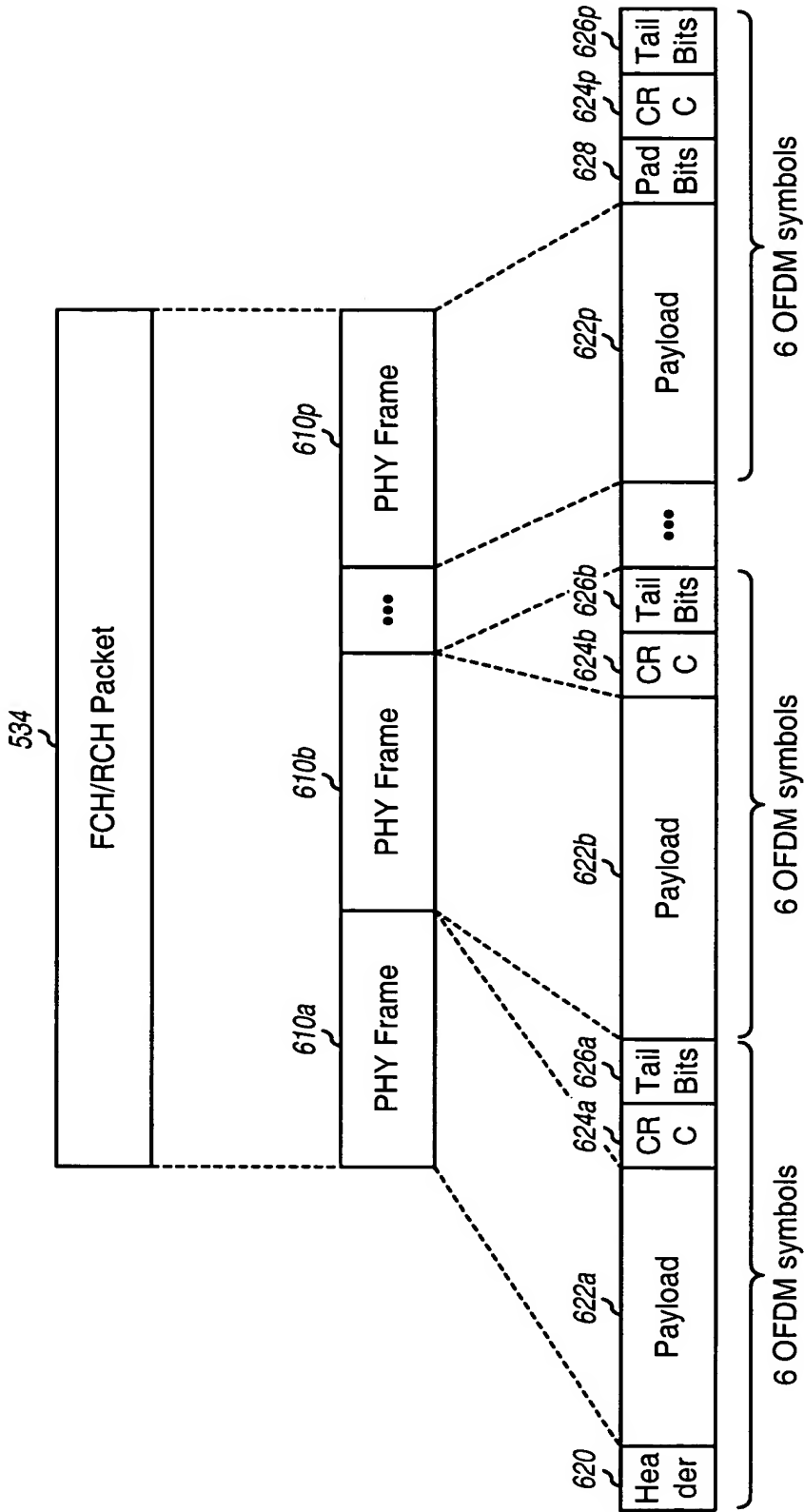


FIG. 6

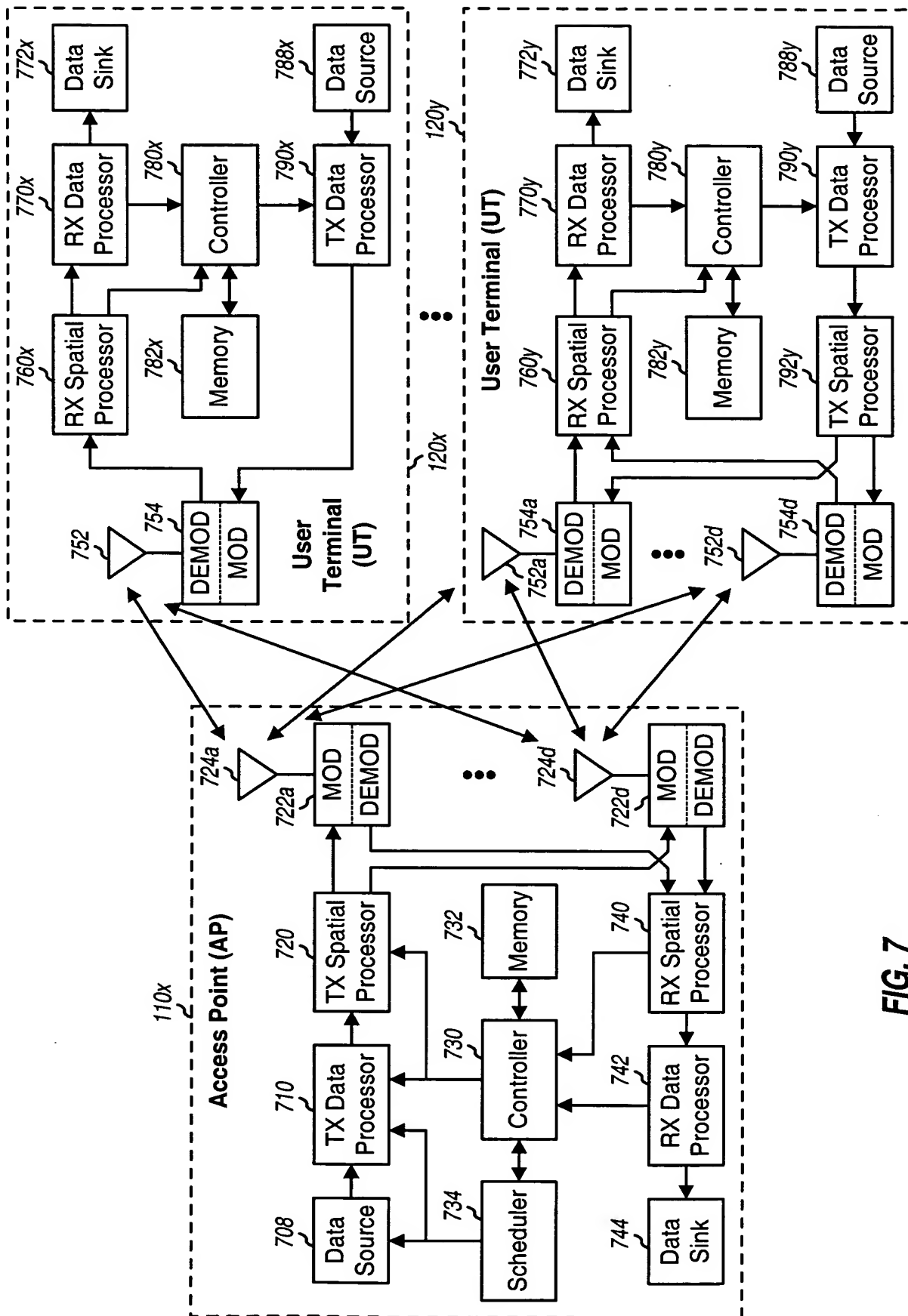


FIG. 7

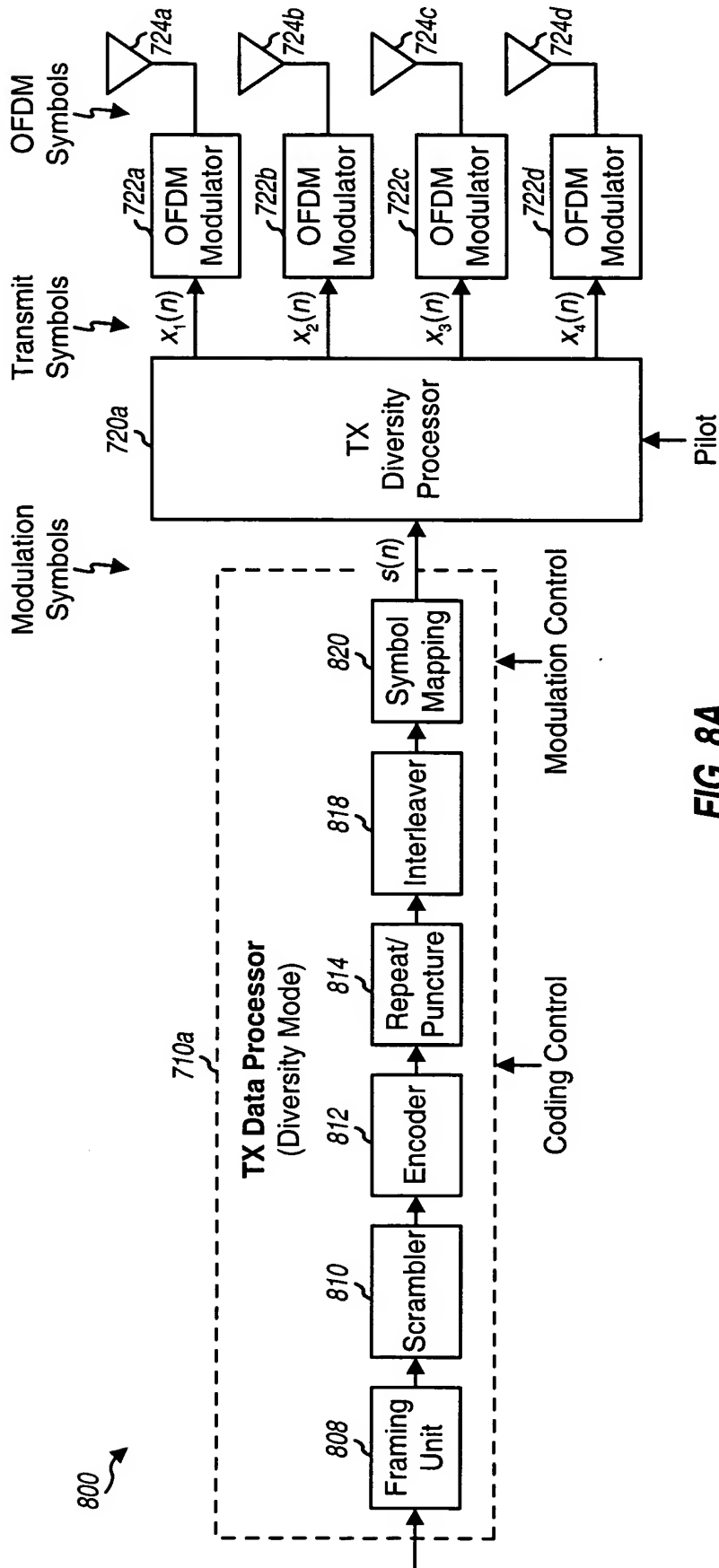


FIG. 8A

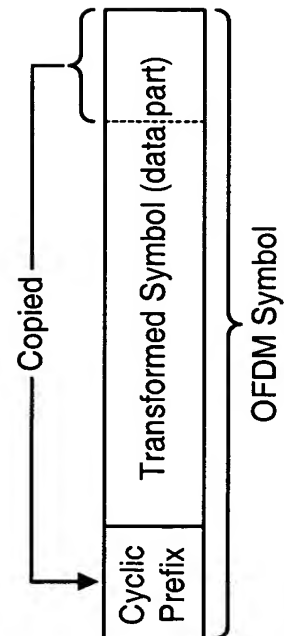


FIG. 8D

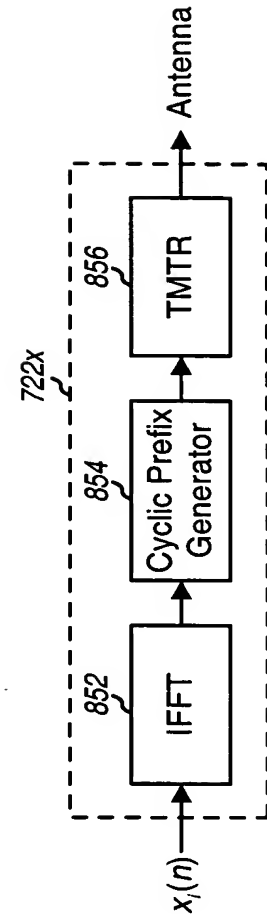


FIG. 8C

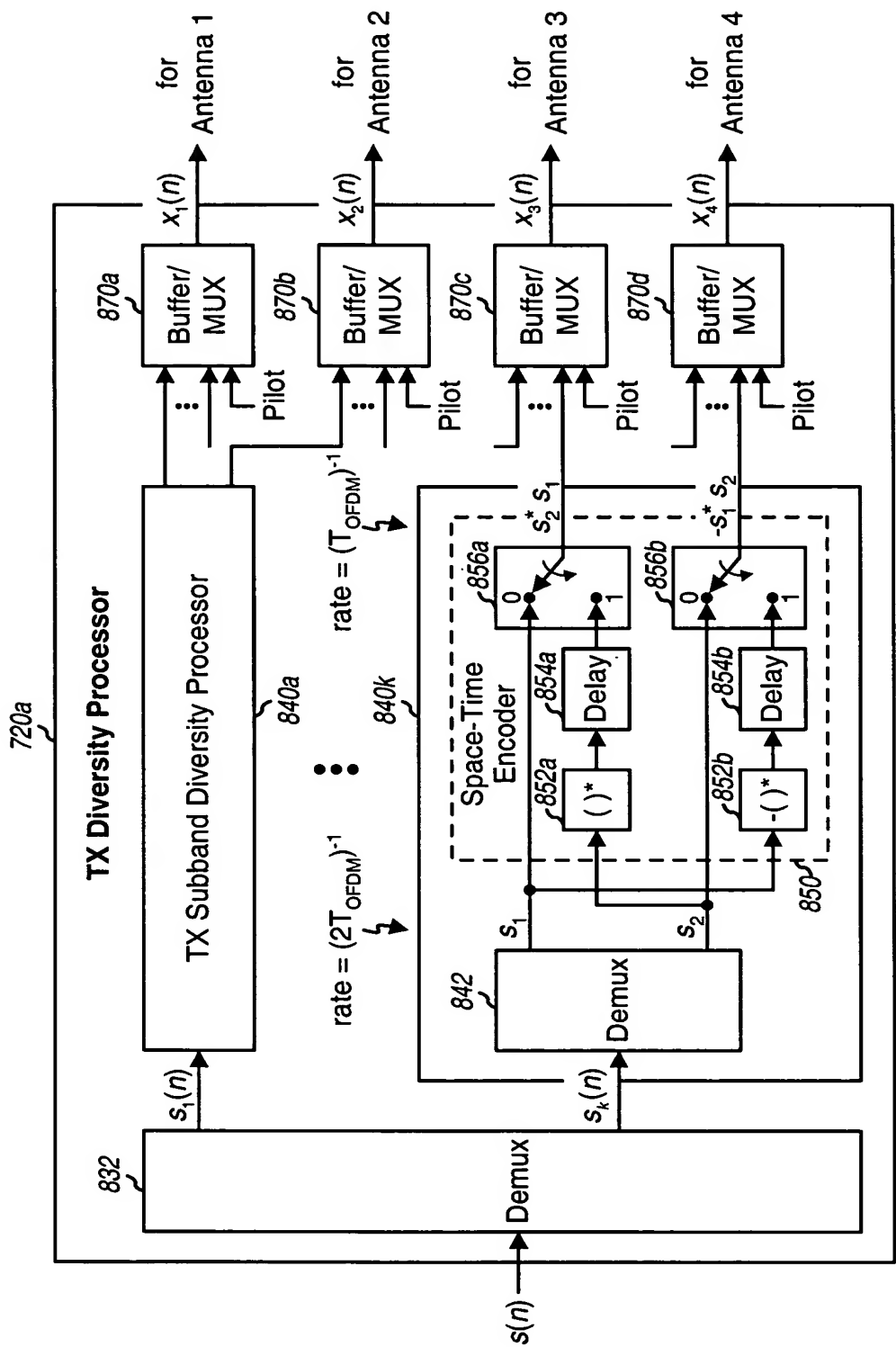


FIG. 8B

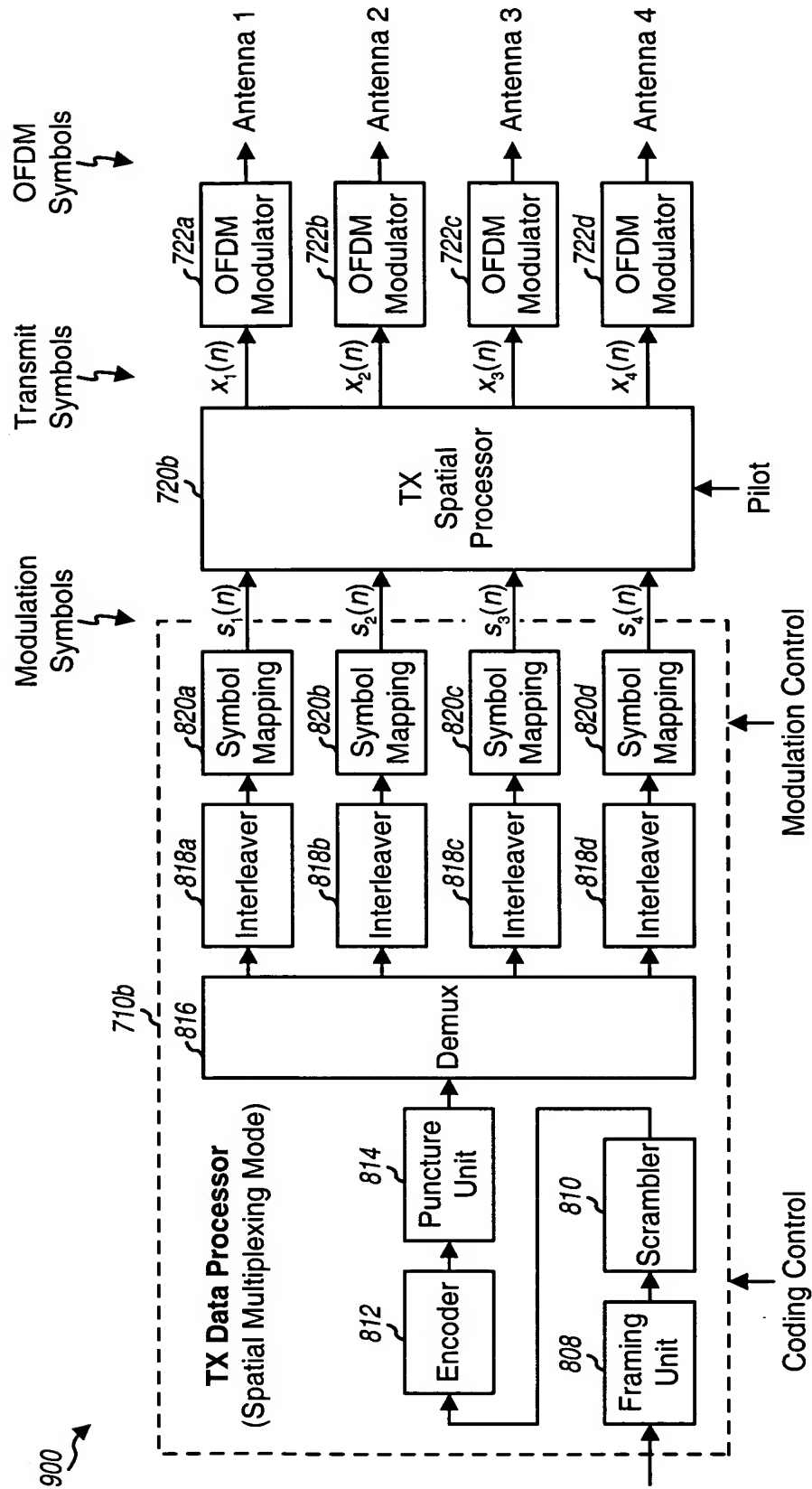


FIG. 9A

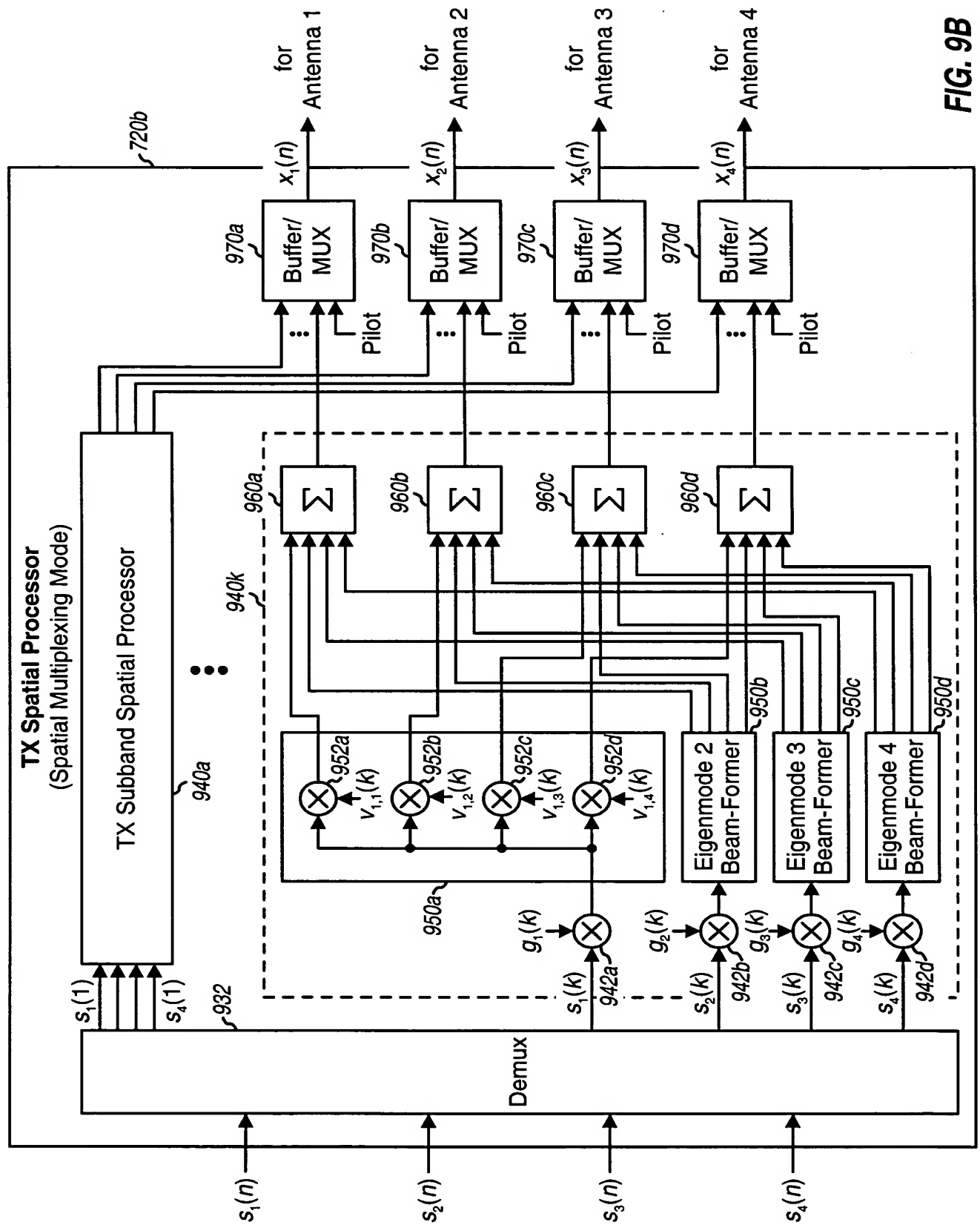


FIG. 9B

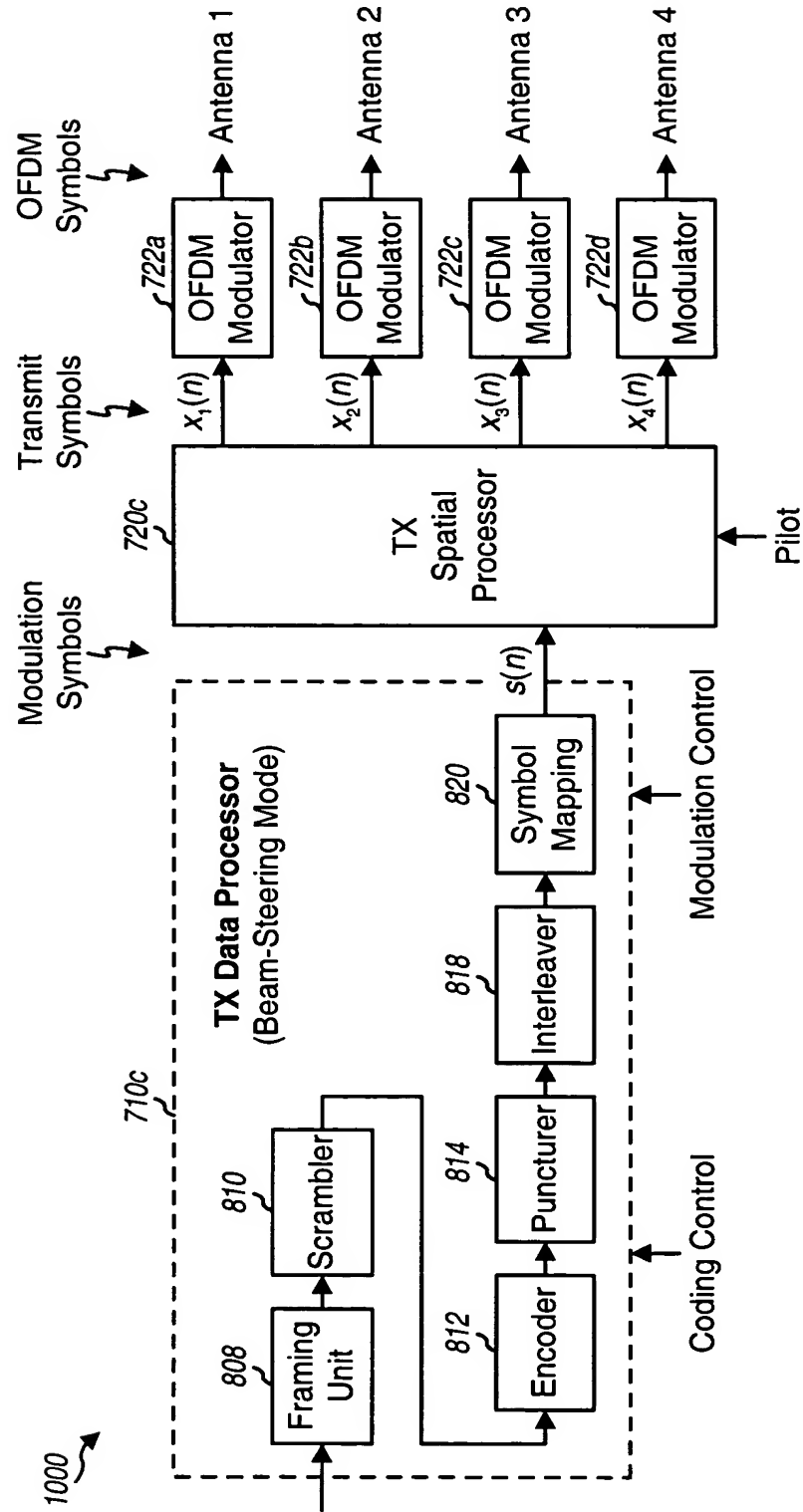


FIG. 10A

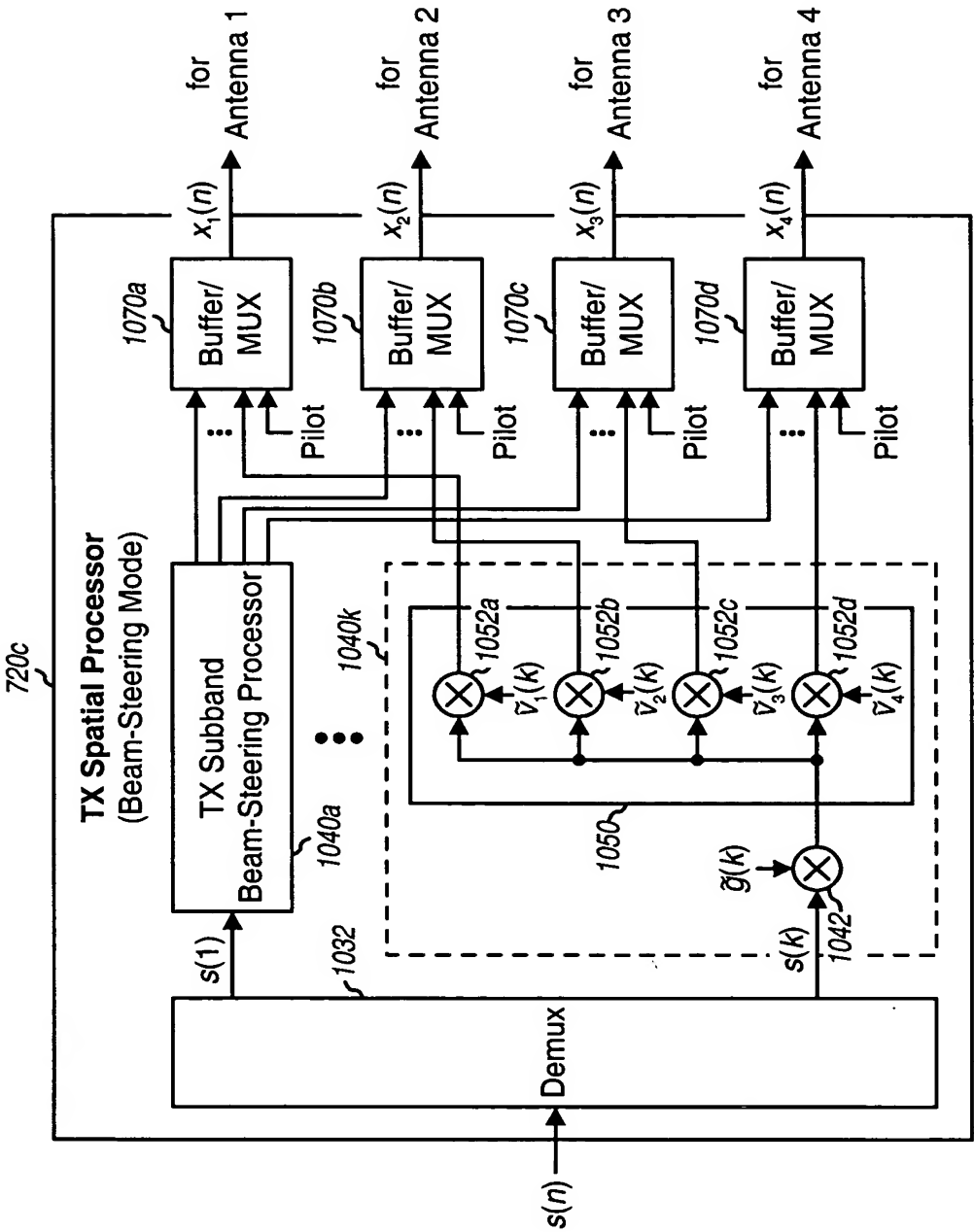


FIG. 10B

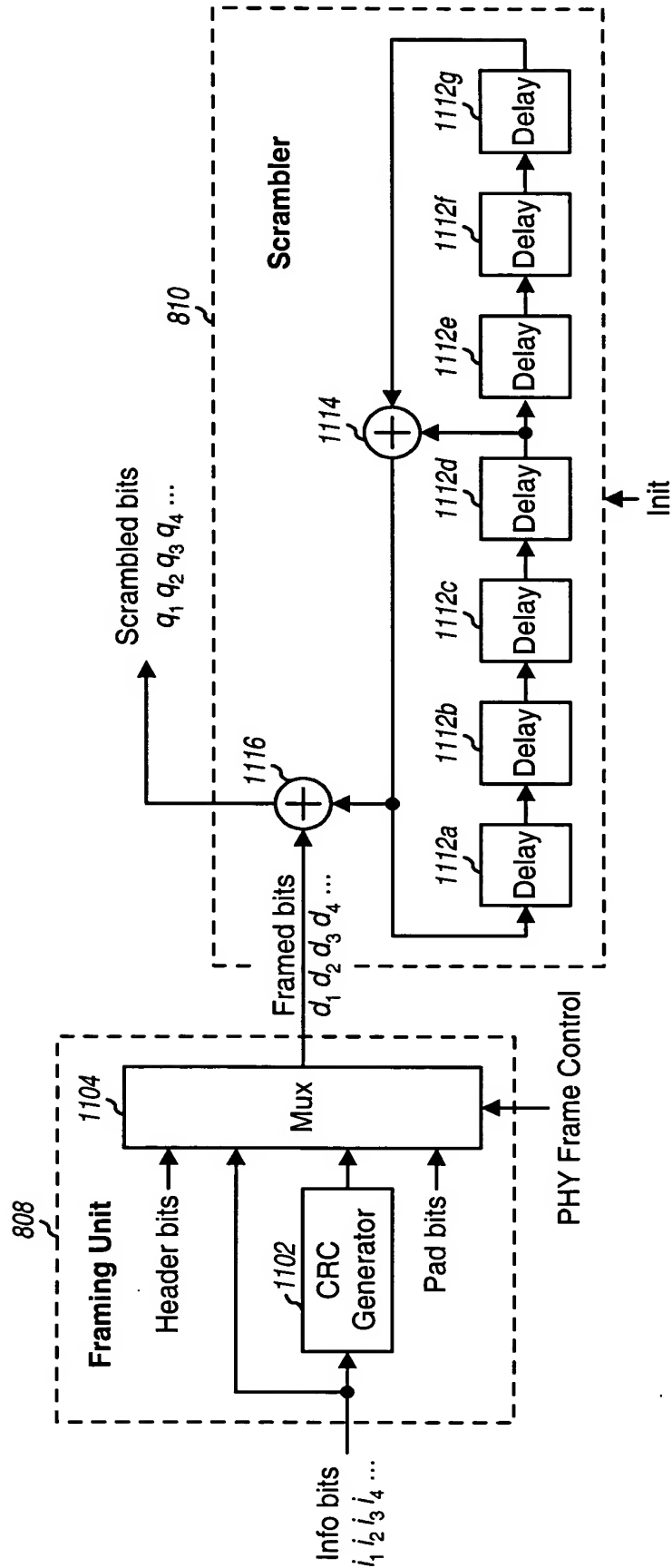


FIG. 11A

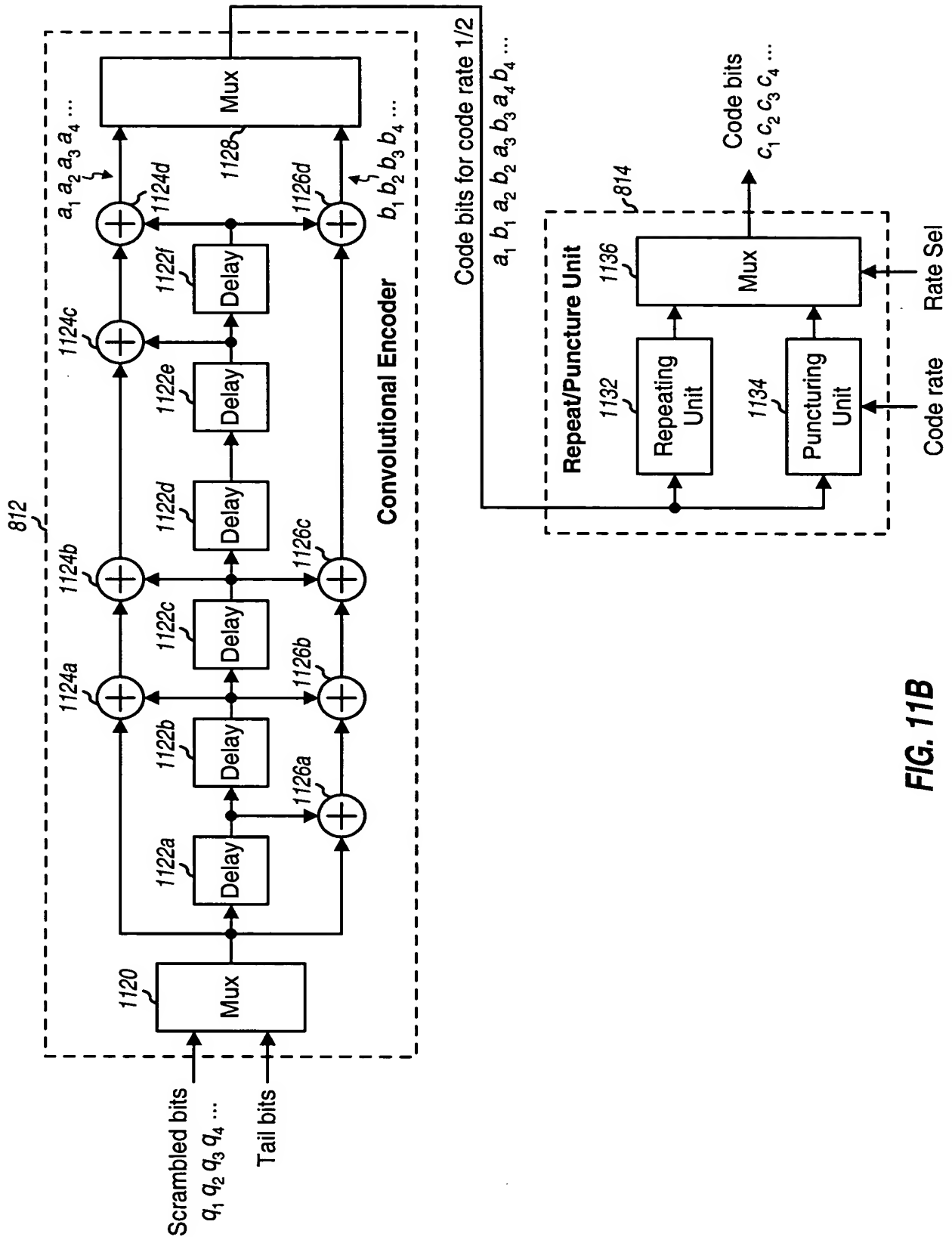


FIG. 11B

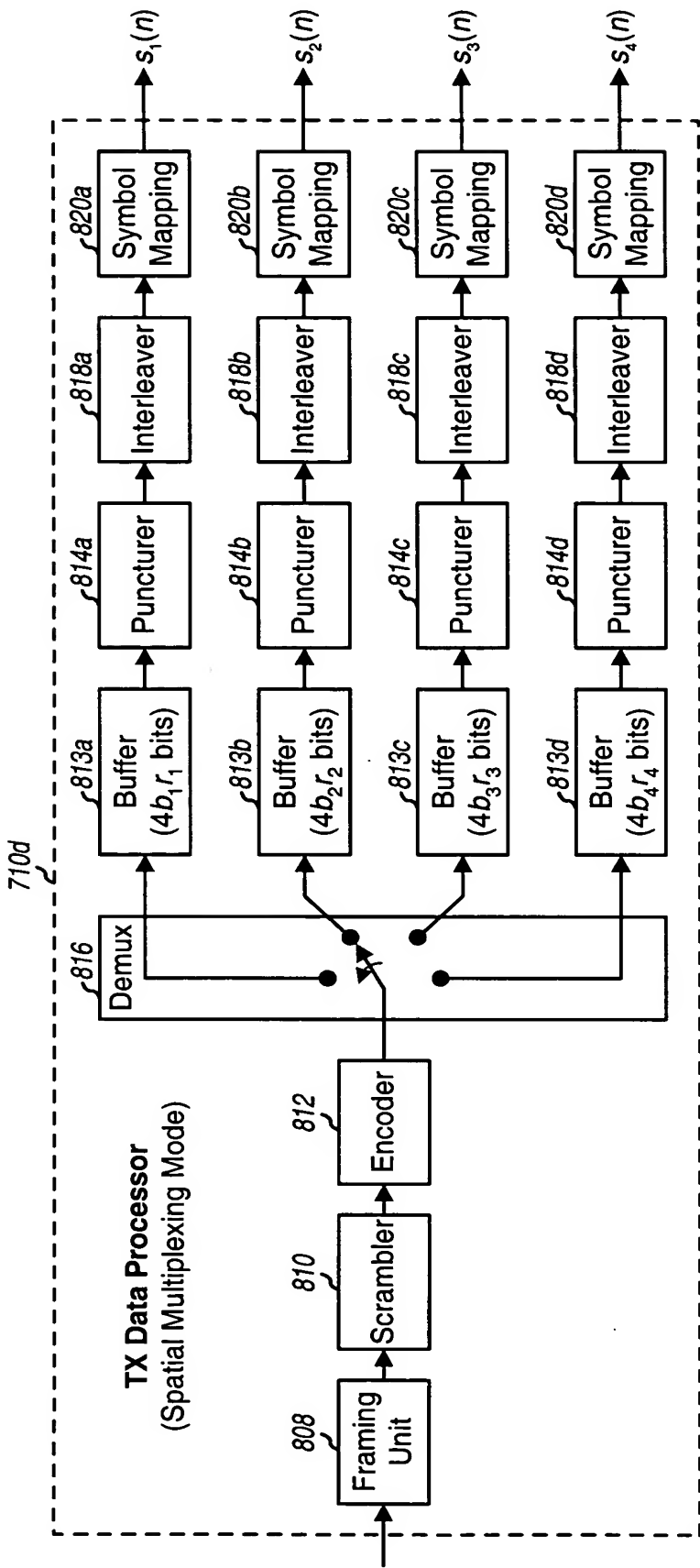


FIG. 11C

Sheet 19/ 23

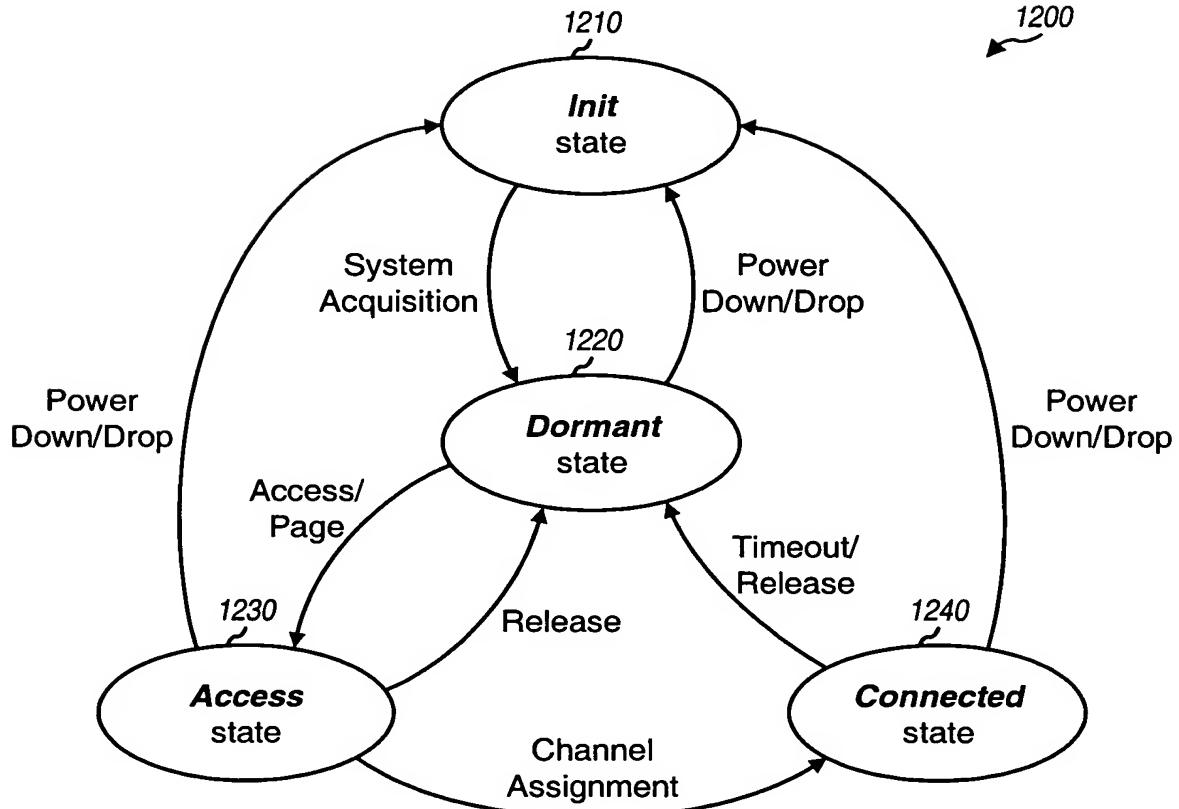


FIG. 12A

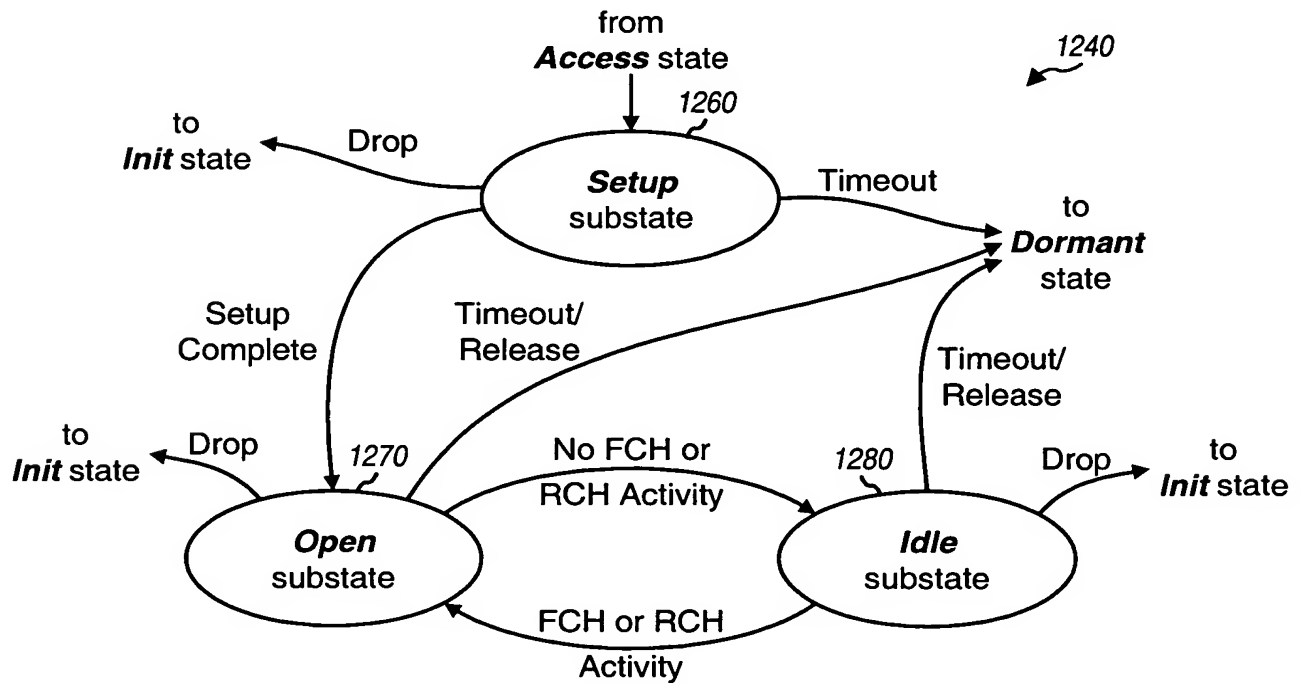


FIG. 12B

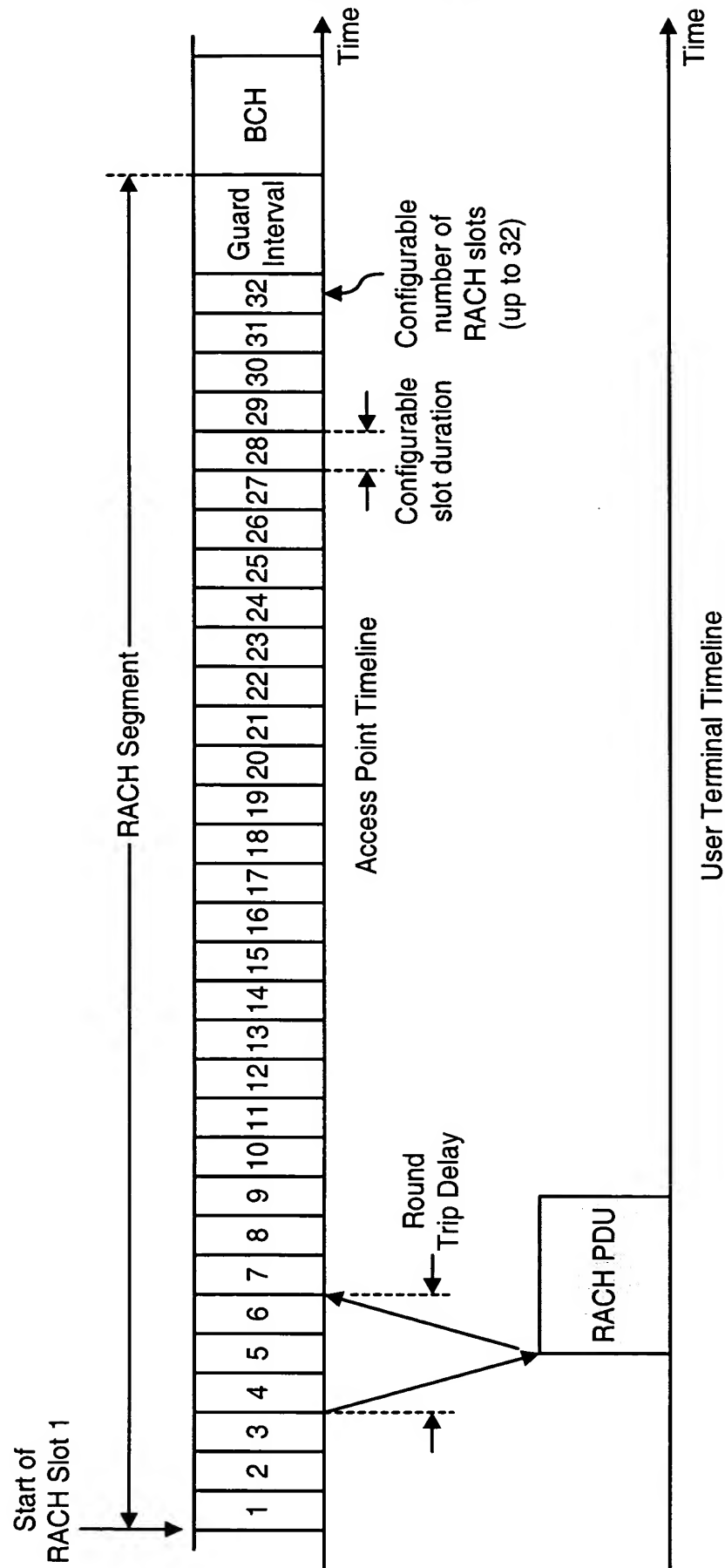


FIG. 13

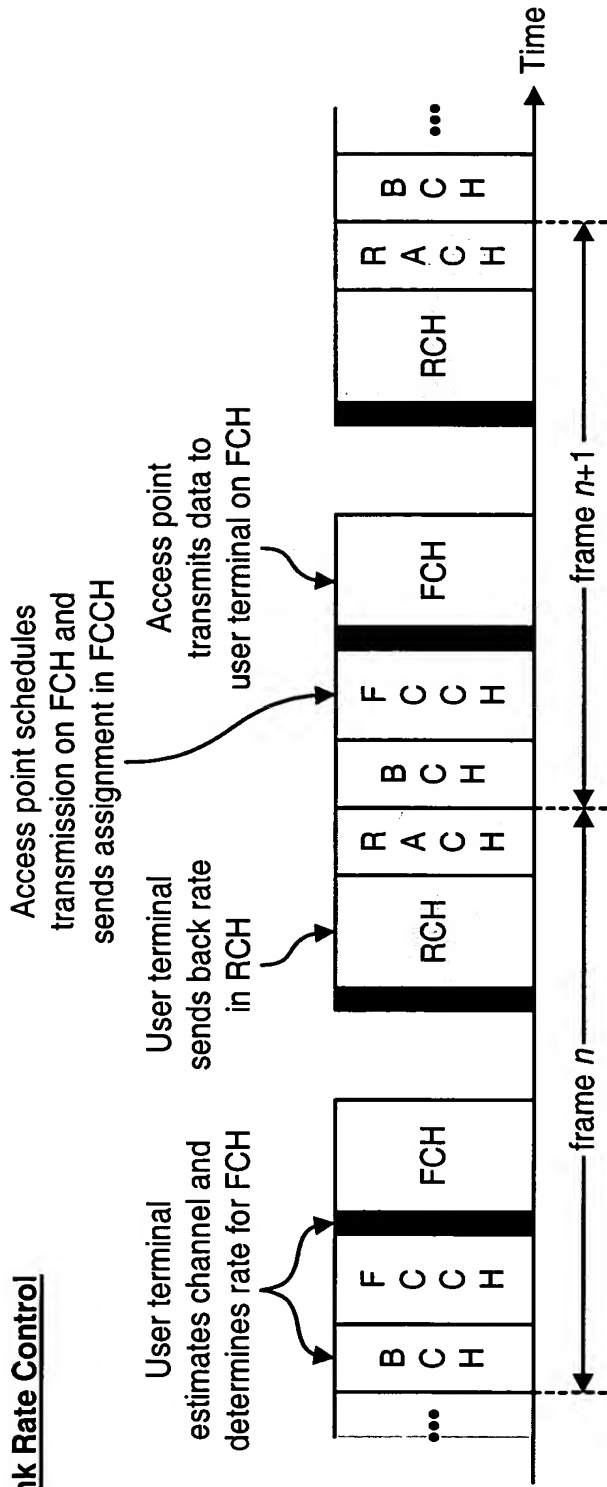
Downlink Rate Control

FIG. 14A

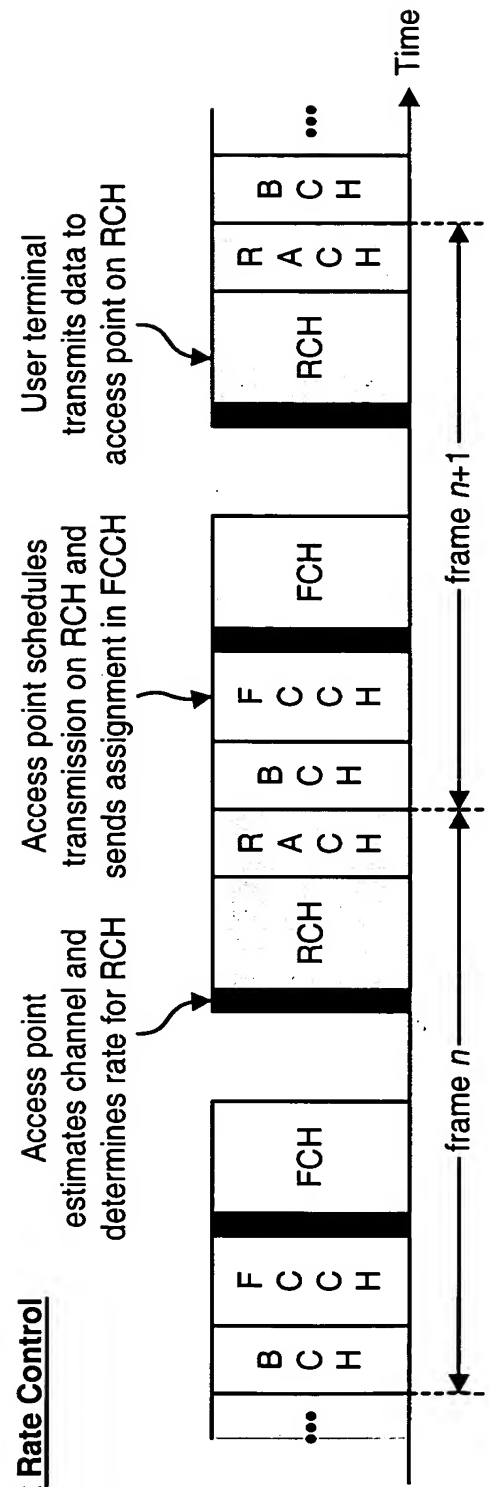
Uplink Rate Control

FIG. 14B

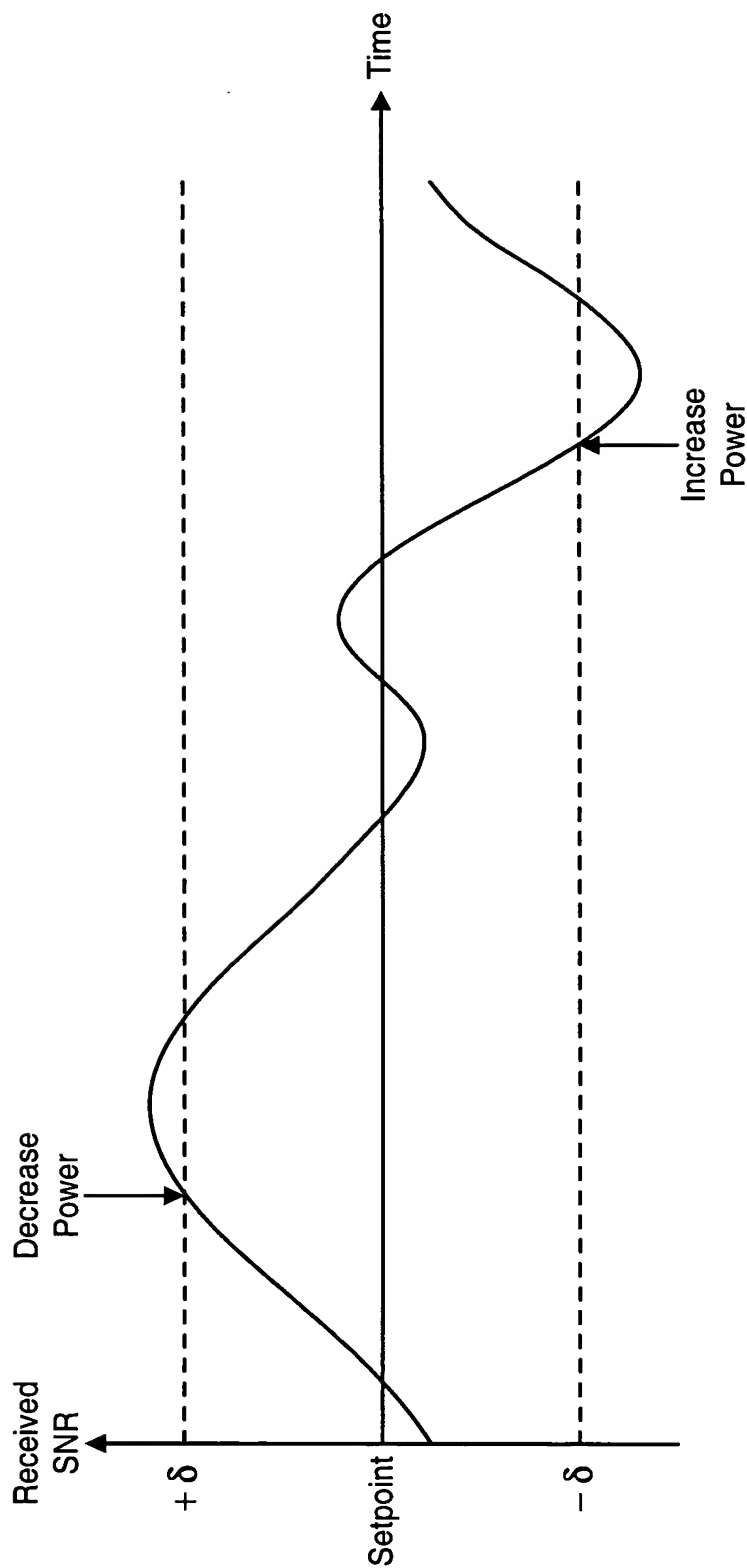


FIG. 15

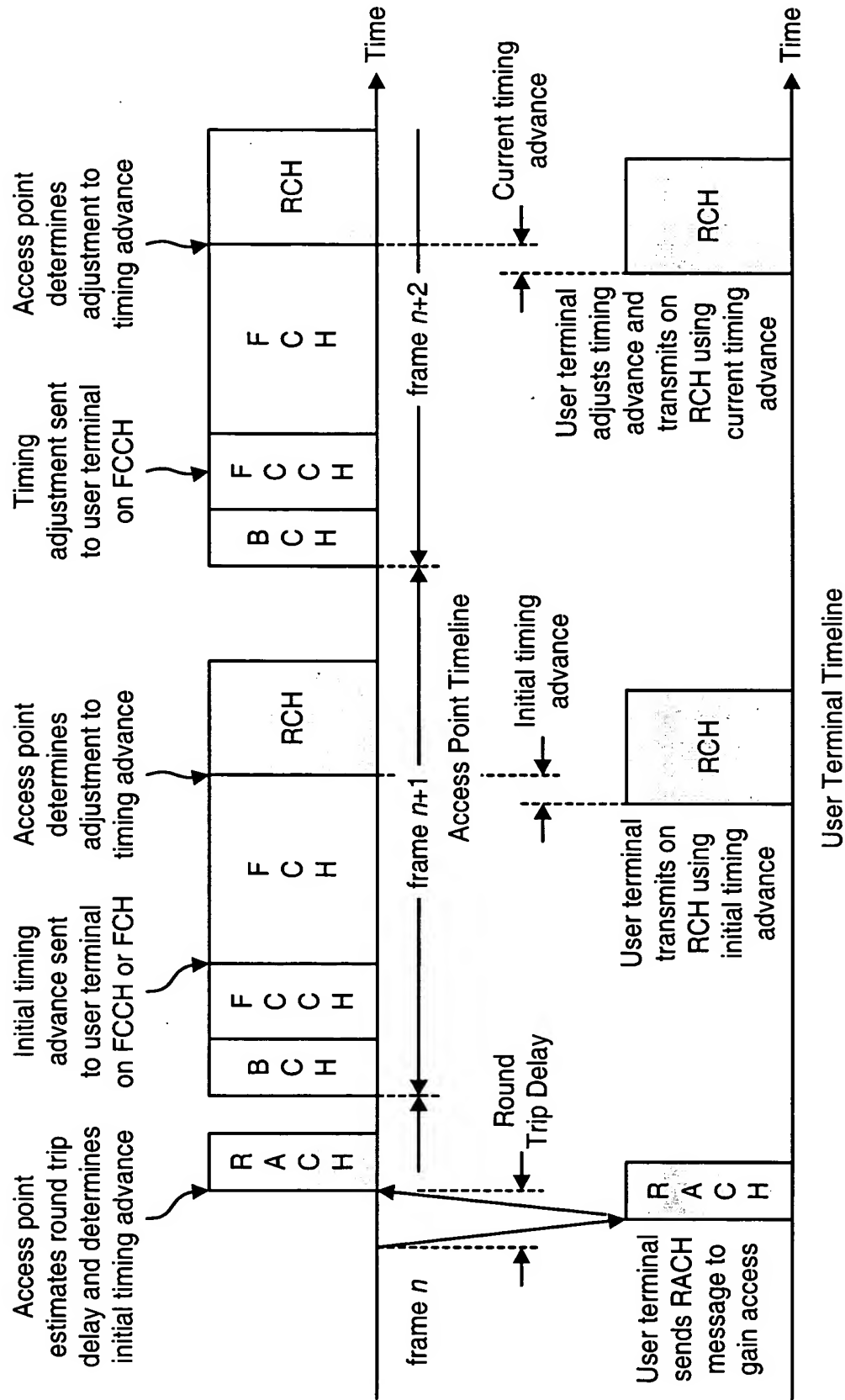


FIG. 16